National Semiconductor

LM1870 Stereo Demodulator with Blend

General Description

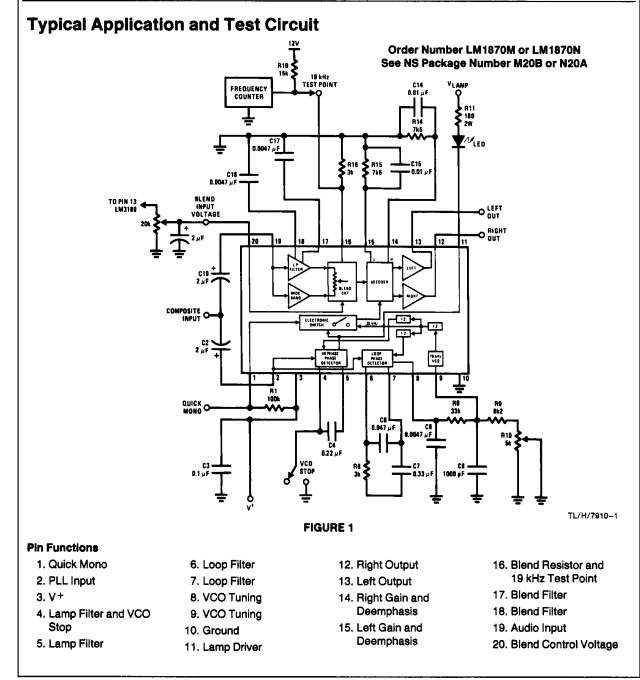
The LM1870 is a phase locked loop FM stereo demodulator with a DC control pin for reducing noise by decreasing separation during weak signal conditions.

Applications

- Automobile radios
- Hi Fi receivers and tuners
- High performance portable radios

Features

- Blend control
- Large input overload
- Low beat note distortion
 Low THD diode switching outputs
- VCO stop function
- Wide supply range, 7V to 15V
- Mono override pin



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Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage, Pin 3	15V
Lamp Driver Voltage, Pin 11	18V
Output Voltage, Pin 12, 13, Supply Off	7V
Quick Mono Input (Pin 1)	V+ (Pin 3)
Blend Input (Pin 20)	15V
Operating Temperature Range	0°C to +70°C
Power Dissipation (Note 1)	1.9W

Storage Temperature	-65°C to +125°C
Soldering Information	
Dual-In-Line Package	
Soldering (10 sec)	260°C
Small Outline Package	
Vapor Phase (60 sec)	215°C
Infrared (15 sec)	220°C
See AN-450 "Surface Mounting	Methods and Their Effect

on Product Reliability" for other methods of soldering surface mount devices.

Electrical Characteristics $T_A = 25^{\circ}C$, $V^+ = 8V$, Figure 1

Parameter	arameter Conditions		ameter Conditions Min Typ		Тур	Max	Units	
DC								
Operating Supply Voltage		7	8	15	v			
Supply Current					mA			
Input DC Voltage	Pin 19	4		v				
Input DC Voltage	Pin 2	1.8		l v				
Supply Rejection		15			dB			
Lamp Leakage Current	Lamp Off, Pin 11 = 16V		0.1	100	μA			
Lamp Saturation Voltage	Lamp On, Pin 11 @ 75 mA		1.4	2.0	v 'v			
VCO Stop Voltage	Voltage at Pin 4 to Stop VCO	0.2	0.4		v			
VCO Stop Current	Pin 4 = 0.2V		-30	- 100	μA			
Blend Input Bias Current	Pin 20 = 0V		-2	-20	μA			
Quick Mono Switch Voltage			4		v v			
Quick Mono Bias Current	Pin 1 = 8V		2		μA			
Output Leakage	Pin 12 or 13 = 6.5V, Pin 3 = 0V		0.1	20	μΑ			
Audio			· · · · ·					
Mono Gain	1 kHz	-4	-1	+2	dB			
Mono THD	1 kHz @ 200 mVrms		0.05	0.25	%			
Channel Balance				±1.5	dB			
Gain Shift	Mono to Stereo	o l		±1.0	dB			
Channel Separation	Pin 20 ≥ 1.1V	30	45		dB			
Output DC Shift	Mono to Stereo		±15	±100	mV			
Input Resistance	Pin 19	20	40		kΩ			
Output Resistance	Pin 12, 13		65	200	Ω			
Ultrasonic Rejection	19 kHz + 38 kHz		30		dB			
SCA Rejection	(Note 2)		70		dB			
Signal to Noise	1 kHz @ 200 mVrms Mono		68		dB			
PLL								
Lamp On Voltage	19 kHz on Pin 2		15	20	mV			
Lamp Off Voltage	19 kHz on Pin 2	2.5	5		mV			
Lamp Hysteresis			10		dB			
Capture Range	25 mVrms on Pin 2	±2	±4	±6	%			
Hold In Range	25 mVrms on Pin 2		±12		%			
Input Resistance	Pin 2	8	14		kΩ			
Blend	Pin 20 from 1.1V to 0.2V							
Stereo Gain Change	1 kHz L = - R Input	-25	-35		dB			
Mono Gain Change	1 kHz L = R Input	-1.5	-0.5	0.5	dB			
	10 kHz L = R Input	-8	- 14	-20	dB			
Output DC Shift			±40	±100	mV			

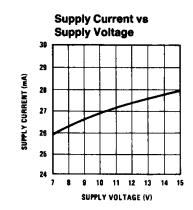
Note 1: For operation in ambient temperatures above 25°C, the device must be derated based on a 150°C maximum junction temperature and a thermal resistance of 65°C/W junction to ambient for the DIP and 75°C/W junction-to-ambient for the small outline package.

Note 2: Input is 10% SCA (74.5 kHz), 9% pilot and 1 kHz left or right. Rejection is ratio of 1 kHz output to 1.5 kHz output.

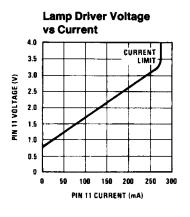
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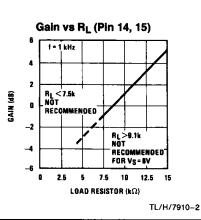
Part #	Recommended	Purpose		Effe	ct	Demerica
	Value		Sm	aller	Larger	Remarks
R1	100k	Pull Up for Quick Mono	ок		Errors Due to Pin 1 Bias Current	Pin 1 Can Be Shorted to Supply if Quick Mono is Not Used
C2	2 μF	PLL Input Coupling	Loading of Source Varies with Frequency			For Sources of Less Than 100Ω , Can use 0.1 μ F
C3	0.1 μF	Supply Bypass				
C4	0.22 μF	Lamp Filter	Shorter Time to Switch Mono to Stereo		Longer Time to Switch Mono to Stereo	High Dielectric Resistance
R6 C6 C7	3k 0.047 μF 0.33 μF	Loop Filter	High Stereo Distortion		Narrower Capture Range	
R8	33k		High Stere	0	Loop Doesn't Lock	
C8	0.0047 μF	Loop Filter	Distortion		Narrower Capture Range	
C9 R9	1000 pF 8.2k	Sets VCO Free Running	High VCO	Jitter	Narrower Capture Range	NPO 5%
R10	5k	Frequency	VCO Not Adjustable with C9		Mətalfilm	
R11	180Ω	Sets Lamp Current	Excess IC Dissipation		Dim Lamp	
R14 R15	7.5k 7.5k	Load Resistors	Low Output Voltage		Output Clips Earlier	
C14 C15	0.01 μF 0.01 μF	Deemphasis				
R16	3k	Sets Blend Chara	acteristic See Curves			
C17 C18	0.0047 μF 0.0047 μF	Filter for Blend	Insufficient Blend	t	Reduced Blend Bandwidth	
C19	2 μF	Audio Input Coupling	Poor Low Fre- quency Response and Separation		Turn On Delay	
R19	15k	Allows VCO Monitoring	Excess IC Dissipation		Reduces 19 kHz Output Voltage	Only Need During Set Up

Typical Performance Characteristics Blend off unless otherwise stated

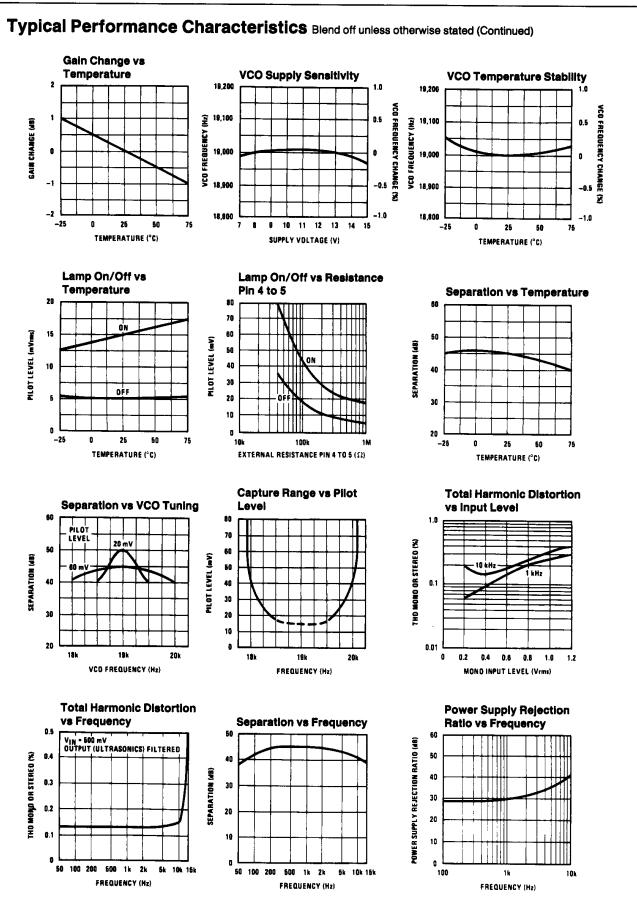


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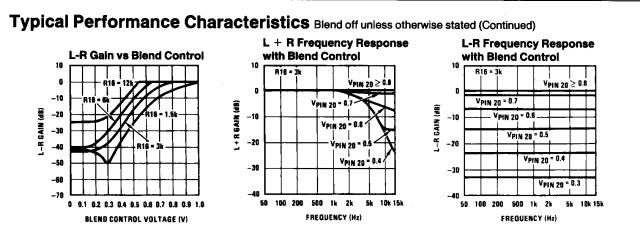




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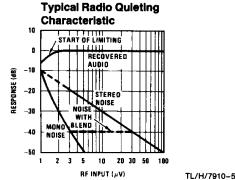
LM1870

Application Hints

Blend—What & Why?

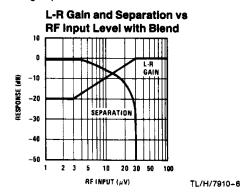
The signal to noise of a weak FM stereo signal is worse than that of an equally weak FM mono signal. For this reason FM mono radios often perform better than FM stereo radios, unless the latter is forced into mono.

The typical quieting curves of an FM stereo radio look like this:

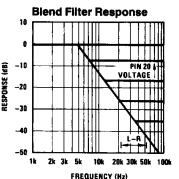


If an acceptable signal to noise is 40 dB, then 20 dB more signal is required in stereo compared to mono, 30 μ V vs 3 μ V. The degradation in noise is due to the L-R or difference channel. If the gain of the L-R is reduced, then the noise associated with it will be reduced. However, there will also be a reduction in separation.

To maintain a 40 dB signal to noise in the above example, the gain of the L-R signal should be reduced from 0 dB gain (a) 30μ V downward to -20 dB at 3μ V. If this is done properly the dashed line will result. Below is a plot of L-R gain and resulting separation.



The LM1870 reduces the gain of the L-R channel before it is demodulated. This is done by a voltage controlled shelving filter. The Bode plot of this filter is shown below:



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The full blend response is a two pole roll-off with each pole set by an internal 6.8k resistor and the capacitance from pins 17 and 18 to ground. The standard value for both capacitors is 4.7 nF resulting in two 5 kHz poles. The blend input (pin 20) is derived from the meter drive output of the FM IF chip (LM3089 or LM3189 pin 13). To adjust for variations in RF gain and other IC parameters, it is recommended that an adjustment be made on each radio.

Mono-Stereo Switching

The LM1870 automatically switches from mono to stereo when the level of pilot at pin 2 is about 15 mV or more. This value can be increased by putting a resistor between pins 4 and 5, as shown graphically in the Typical Performance curves.

If it is desired to switch to mono without turning off the lamp driver, pin 1 should be taken below 4V. This is a high impedance input that can be electronically switched by a transistor with a pull up resistor to the IC supply.

Outputs

The LM1870 has emitter-follower outputs resulting in a low output impedance. The output will sink or source one mA, therefore it will drive AC coupled loads greater than 2 k Ω .

In AM-FM radios the switching can be cumbersome at best. To ease the problem the outputs of the LM1870 (pins 12 and 13) are open circuit when the supply (pin 3) is open or grounded. This reduces the number of switch poles required

Application Hints (Continued)

since the outputs can remain connected at all times. This technique is commonly called diode switching but the method used in the LM1870 results in substantially lower distortion than obtained with discrete diodes.

VCO

The stereo performance of the LM1870 is very constant for small (<2%) changes in the free running frequency of the VCO. To insure that the frequency stays within 2%, low temperature coefficient components should be used for the tuning capacitor (1000 pF) and resistor (8.2k). The internal oscillator has a temperature coefficient of about 50 ppm/°C (see curve). With an NPO capacitor and a metalfilm resistor the total variation in the free running frequency will be less than 1% over the full temperature range. Tuning the VCO is done by adjusting the 5 k Ω potentiometer to get 19 kHz \pm 50 Hz with no input on pin 2.

The VCO frequency is monitored at pin 16 when current is supplied to the pin. During normal operation the 19 kHz square wave is not available and the resistor from pin 16 to ground programs the blend characteristics (see curves).

The VCO of the LM1870 can be stopped by taking pin 4 low. In addition to being useful for turning off the stereo indicator and forcing mono FM reception, this also allows other mono sources, such as AM, to be fed into the decoder and come out both channels. The signal will not be inadvertently decoded with the VCO off and it will have the same gain and balance characteristics as the FM. The deemphasis capacitors may need to be removed for proper frequency response. The voltage on pin 20 will also affect the freugency response.

It should be noted that a stopped VCO cannot radiate into the rest of the radio and cause interference. Pin 4 can be taken low with a mechanical switch or an NPN transistor. If a transistor is used it must have low leakage, less than 100 nA at 3V V_{CE}, and low saturation, less than 200 mV at 100 μ A collector current.

PLL

To properly demodulate the L-R signal the decoder must generate a 38 kHz signal that is locked in phase with the 19 kHz pilot signal at the input. This is done with a phase locked loop consisting of a phase detector, a loop filter (pins 6 and 7) and a VCO (pins 8 and 9).

The loop filter is similar to other standard decoders however the VCO incorporates an additional low pass filter (4.7 nF and 33 $k\Omega$) to reduce beat note distortion an additional 20 dB.

Input Interface

There are two inputs to the LM1870, one for the PLL (pin 2) and the normal audio input (pin 19). The input impedance of the audio input is about 40 k Ω . The input coupling capacitor works with this input resistance and sets the low frequency response and separation.

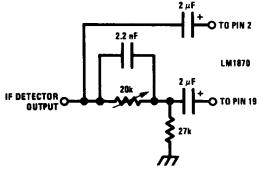
The PLL input (pin 2) locks onto the 19 kHz pilot and rejects the rest of the composite signal. For this reason it is only necessary to use a coupling capacitor large enough to insure there is no phase shift at 19 kHz. The input resistance of the PLL is 14 k Ω so a capacitor between 0.01 μ F and 0.1 μ F would be fine. However, the source driving this input must not be affected by this load. This is true only when the source is low impedance (less than 100 Ω).

Typical FM IF circuits have detector output impedance of 5 k Ω or more. This will cause very poor low frequency response and separation unless the loading is made constant over frequency. For this reason the typical input coupling capacitor is 2 μ F.

IF Correction

The separation in most radios is limited by the response of the IF. The input lead network below can often be used to improve radio separation.

IF Correction Lead Network



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Power Supply

The LM1870 is designed to work on supplies from 7V to 15V. For automotive applications a regulator is recommended to protect against transients; the LM2930-8V is the ideal choice. This datasheet has been downloaded from:

www.DatasheetCatalog.com

Datasheets for electronic components.

National Semiconductor was acquired by Texas Instruments.

http://www.ti.com/corp/docs/investor_relations/pr_09_23_2011_national_semiconductor.html

This file is the datasheet for the following electronic components:

LM1870N - http://www.ti.com/product/Im1870n?HQS=TI-null-null-dscatalog-df-pf-null-wwe LM1870M - http://www.ti.com/product/Im1870m?HQS=TI-null-null-dscatalog-df-pf-null-wwe